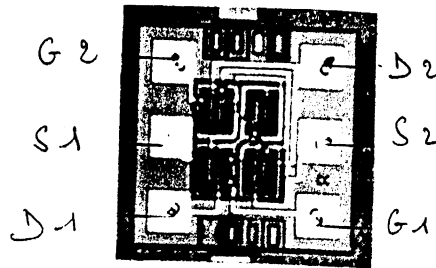




# Sierra Components, Inc.

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Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.



**Topside Metal:** Al ( Aluminum)  
**Backside:** Au ( Gold)  
**Backside Potential:**  
**Mask Ref:**  
**Bond Pads (Mils):**

APPROVED BY:   
MFG: Siliconix

**DIE SIZE (Mils):** 21 x 21  
**THICKNESS:** 8

**DATE:** 3/4/99  
**P/N:** U423NNT

DG 10.1.2  
Rev A 3-4-99